

PT40QPx45

Pulse Power Thyristor Switch

DS6190-3 June 2016 (LN33533)

FEATURES

- Double Side Cooling
- Fast turn-on
- Low Turn-on Losses

APPLICATIONS

- Pulse Power
- Crowbars
- Ignitron replacement

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DRM} and V _{RRM} V	Conditions
PT40QPx45	4500/16	$ \begin{split} T_{vj} &= 0^{\circ}C \text{ to } 125^{\circ}C, \\ I_{DRM} &= I_{RRM} = 50\text{mA}, \\ V_{DRM}, V_{RRM} t_p &= 10\text{ms}, \end{split} $

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

PT40QPx45

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

V _{DRM}	4500V
I _{T(AV)}	760A
I _{TSM}	13000A
dl/dt	5000A/µs

* Higher dV/dt selections available

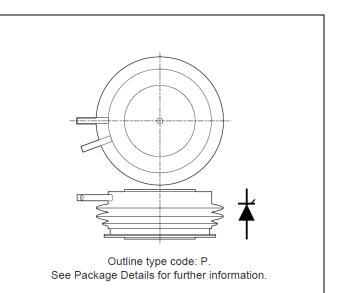


Fig. 1 Package outline

CURRENT RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{T(AV)}	Mean on-state current	Half wave resistive load, T _{case} = 80°C	760	А
I _{T(RMS)}	RMS value	-	1190	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	rge (non-repetitive) on-state current 10ms half sine, T _{case} = 125°C		kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM}$	0.541	MA ² s
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}C$	13	kA
l ² t	I ² t for fusing	$V_R = 0$	0.845	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	5	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.033	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 11kN	Double side	-	0.008	°C/W
-		Blocking V_{DRM} / V_{RRM}		-	125	°C
T _{vj}	Virtual junction temperature	On-state (conducting)		-	135	°C
T _{stg}	Storage temperature range			-55	125	°C
F _m	Clamping force			11.0	15.0	kN

DYNAMIC CHARACTERISTICS

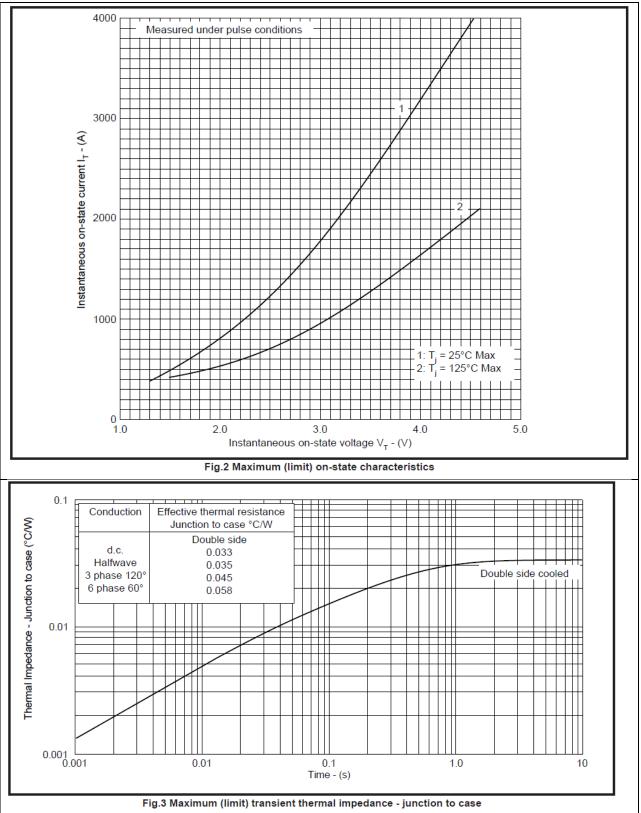
Symbol	Parameter	Test Conditio	ns	Min.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 125°C		-	50	mA
dV/dt	Max. linear rate of rise of off-state voltage To 67% V_{DRM} , $T_j = 125^{\circ}C$, $R_{gk} \le 1.5\Omega$		-	200	V/µs	
dl/dt	Rate of rise of on-state current	From 67% V_{DRM} to 20kA Gate source 30A $t_r < 1.5\mu s, T_j = 125^{\circ}C$	Non-repetitive	-	5000	A/µs
V _{T(TO)}	Threshold voltage	T _{vj} = 125°C		-	1.4	V
г _т	On-state slope resistance – Low level	T _{case} = 125°C		-	0.152	mΩ

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Тур.	Max.	Units
V _{GT}	Gate trigger voltage	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	1.0	4.0	V
I _{GT}	Gate trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	-	1.5	А

G BYNEX

CURVES





ORDERING INFORMATION

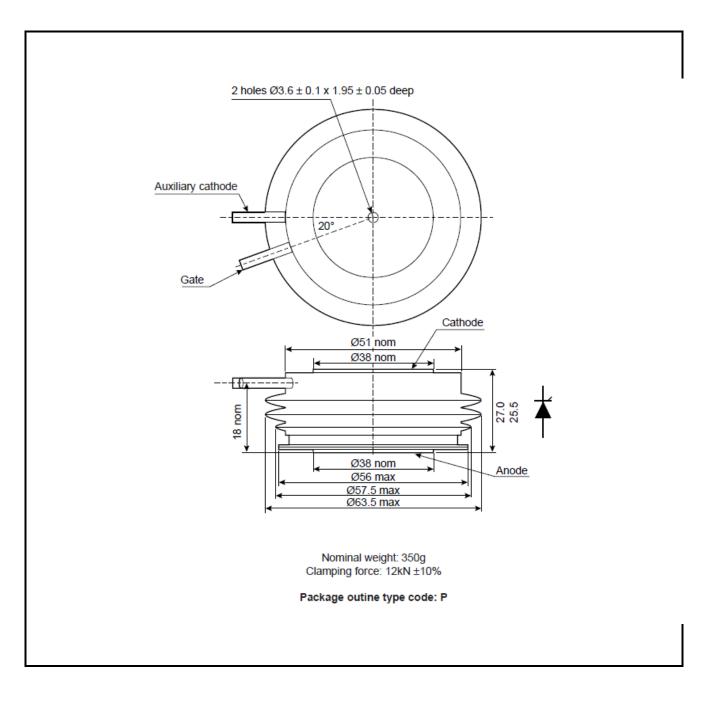
- PT Pulse Power Thyristor
- 40Q Device type P Package out
- Package outline type code
- x lead length (see table, right)45 Voltage x100

Lead length (x)			
0	No lead		
С	8"	200mm	
D	10"	250mm	
E	12"	300mm	
F	16"	400mm	
G	18"	450mm	
Н	20"	500mm	
J	24"	600mm	
К	30"	750mm	
L	40"	1000mm	



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





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